

STD13003Q

NPN Silicon Power Transistor

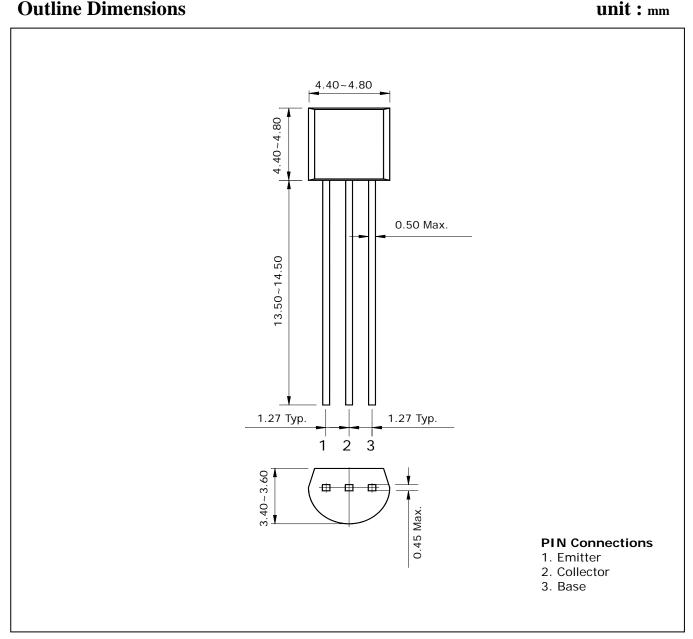
Features

- High speed switching
- $V_{CEO(sus)} = 400V$
- Suitable for Switching Regulator and Motor Control

Ordering Information

| Type NO. | Marking | Package Code | | |
|-----------|----------|--------------|--|--|
| STD13003Q | STD13003 | TO-92 | | |

Outline Dimensions



KSD-T0A012-000

STD13003Q

Absolute Maximum Ratings

(Ta=25℃)

| Characteristic | Symbol | Ratings | Unit |
|-----------------------------|------------------|---------|------|
| Collector-base voltage | V_{CBO} | 700 | V |
| Collector-emitter voltage | V_{CEO} | 400 | V |
| Emitter-base voltage | V_{EBO} | 9 | V |
| Collector current (DC) | I _C | 1.5 | А |
| Collector current (Pulse) | I _{CP} | 3 | А |
| Base current (DC) | I _B | 0.75 | А |
| Collector power dissipation | P _C | 1.1 | W |
| Junction temperature | T _j | 150 | °C |
| Storage temperature | T _{stg} | -55~150 | °C |

Electrical Characteristics

(Ta=25℃)

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|--------------------------------------|------------------------|--|------|------|------|------|
| Collector-emitter sustaining voltage | $V_{CE(sus)}$ | $I_C=5\text{mA},\ I_B=0$ | 400 | - | - | V |
| Collector cut-off current | I _{CBO} | V _{CB} =700V, I _E =0 | - | - | 10 | uA |
| Emitter cut-off current | I _{EBO} | $V_{EB} = 9V, I_{C} = 0$ | - | - | 10 | uA |
| DC current gain | h _{FE} * | I _C =0.5A, V _{CE} =2V | 8 | - | 40 | |
| | | I _C =1A, V _{CE} =2V | 5 | - | - | |
| Collector-emitter saturation voltage | V _{CE(sat)} * | $I_C = 0.5A, I_B = 0.1A$ | - | - | 0.5 | V |
| | | $I_C = 1A$, $I_B = 0.25A$ | - | - | 1 | |
| | | I _C =1.5A, I _B =0.5A | - | - | 3 | |
| Base-emitter saturation voltage | V _{BE(sat)} * | I _C =0.5A, I _B =0.1A | - | - | 1 | V |
| | | I _C =1A, I _B =0.25A | - | - | 1.2 | |
| Transition frequency | f _T | V _{CB} =10V, I _C =0.1A, f=1MHz | 4 | - | - | MHz |
| Output capacitance | C _{ob} | V _{CB} =10V, I _E =0, f=0.1MHz | - | 13 | - | pF |
| Turn on Time | t _{on} | INPUT IBL OUTPUT IBL IBLE - IBE - 200mA DUTY CYCLE ≤1% | - | - | 0.5 | μs |
| Storage Time | t _{stg} | | - | - | 4 | |
| Fall Time | t _f | | - | - | 0.7 | |

^{*} Pulse test: PW \leq 300 μs , Duty cycle \leq 2% Pulse

Electrical Characteristic Curves

Fig. $1 P_C - T_a$

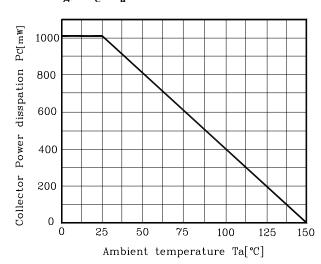


Fig. 2 I_C - V_{CE}

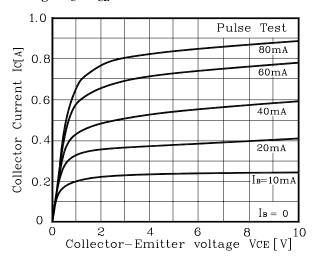


Fig. 3 $V_{\text{CE(sat)}}$ - I_{C}

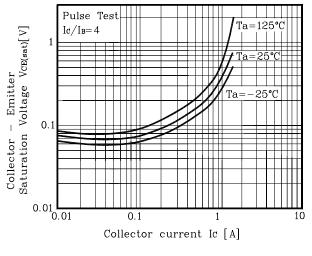


Fig. 4 $V_{BE(sat)}$ - I_{C}

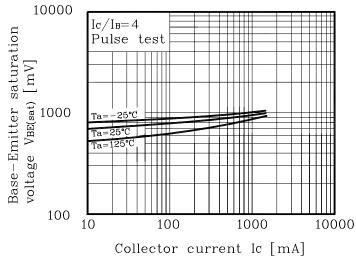


Fig. 5 h_{FE} . I_{C}

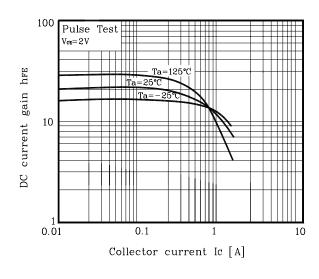
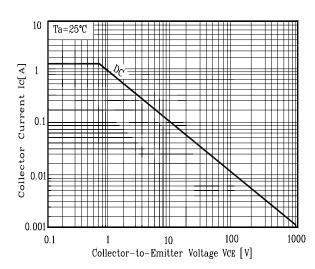


Fig. 6 Safe Operating Area



Electrical Characteristic Curves

Fig. 7 Turn on time

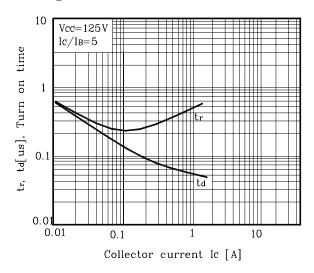
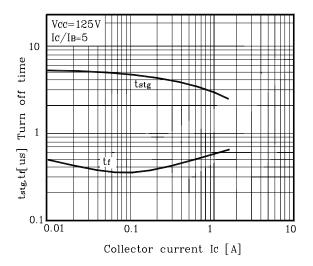


Fig. 8 Turn off time



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